# V40D120C-M3, V40D120CHM3

Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.45$  V at  $I_F = 5$  A



www.vishay.com

Top View

## V40D120C PIN 1 O

		0
PIN 2	$\circ$	HEATSINK

**Bottom View** 

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	250 A			
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.64 V			
T <sub>J</sub> max.	150 °C			
Package	TO-263AC (SMPD)			
Diode variations	Dual common cathode			

## **FEATURES**

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, inductrial, and automotive application.

## **MECHANICAL DATA**

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V40D120C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	120	V	
Maximum average forward rectified current	per device	I <sub>F(AV)</sub>	40	٨	
(fig. 1)	per diode		20	A	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	250	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		TJ, T <sub>STG</sub>	-40 to +150	°C	

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COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.51	-	- V
	I <sub>F</sub> = 10 A			0.63	-	
Instantaneous forward voltage per diade	I <sub>F</sub> = 20 A			0.78	0.89	
Instantaneous forward voltage per diode	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.45	-	
	I <sub>F</sub> = 10 A			0.55	-	
	I <sub>F</sub> = 20 A			0.64	0.71	
	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	11	-	μA
Reverse current at rated $V_{\rm R}$ per diode		T <sub>A</sub> = 125 °C		9.4	-	mA
neverse current at fated v <sub>R</sub> per diode	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	500	μA
		T <sub>A</sub> = 125 °C		23	70	mA

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER		SYMBOL	V40D120C	UNIT
	per diode	- R <sub>θJC</sub>	1.9	°C/W
Typical thermal resistance	per device		1.0	
	per device	R <sub>0JA</sub> <sup>(1)(2)</sup>	45	

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SMPD	V40D120C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
SMPD	V40D120CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel	

#### Note

<sup>(1)</sup> AEC-Q101 qualified

## **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

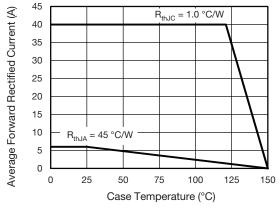


Fig. 1 - Forward Current Derating Curve

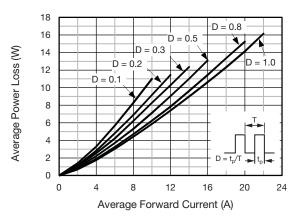


Fig. 2 - Forward Power Loss Characteristics Per Diode

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Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

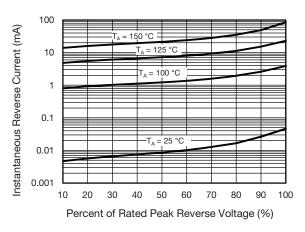


Fig. 4 - Typical Reverse Characteristics Per Diode

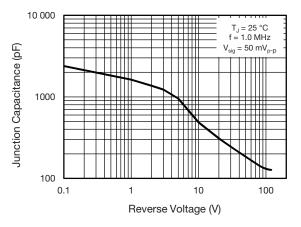


Fig. 5 - Typical Junction Capacitance Per Diode

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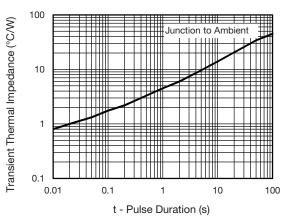


Fig. 6 - Typical Transient Thermal Impedance Per Device

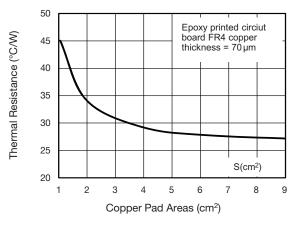


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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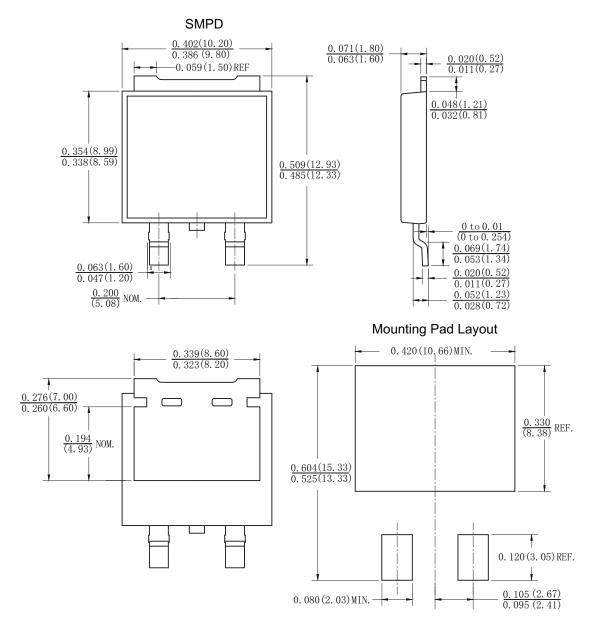
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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